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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Li et al.
Serial No. : 10/729,859
Filed : December 5, 2003
Title : MAGNETIC FIELD EFFECT TRANSISTOR, LATCH AND METHOD
Docket : NAY 0001 PA/31558.2
Art Unit : 2811

CERTIFICATE OF MAILING

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James E. Beyer Reg. No. 39,564

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

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UNDER 37 CFR §§ 1.56, 1.97, AND 1.98

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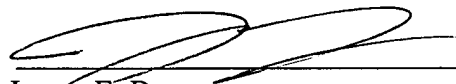
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This Information Disclosure Statement is being filed within the period set forth in 37 CFR §1.97(b) because it is believed to be filed before the mailing date of a first office action on the merits.

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Respectfully submitted,
DINSMORE & SHOHL LLP

By


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Sheet	1	of	2
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Application Number	10/729,859
Filing Date	December 5, 2003
First Named Inventor	Zhiging LI
Art Unit	2811
Examiner Name	not assigned yet
Attorney Docket Number	NAY 0001 PA/31558.2

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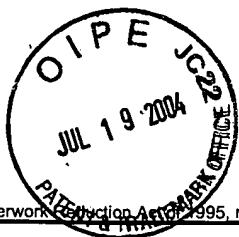
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Date
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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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Examiner Name	Not assigned yet
Attorney Docket Number	NAY 0001 PA/31558.2

Sheet 2 of 2

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		STEFAN ROHRER et al., "A MAGFET Sensor Array for Digital Magnetic Signal Reading", IEEE, 1997, p. 308-310	
		F.J. KUB et al., "Multiple-Gate Split-Drain MOSFET Magnetic-Field Sensing Device and Amplifier", IEDM 1992, p. 517-20.	
		LI, Z. et al., "A CMOS Magnetic Latch with Extremely High Resolution", IEDM 2002, Tech. Digest, pp. 909-12.	
		LI, Z. et al., "Weak Magnetic Field Pattern Detection by CMOS Magnetic Latch", IEEE, 2003.	

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